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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Ko, *et al.* Docket No.: TSM03-0615  
Serial No.: 10/729,095 Art Unit: 2818  
Filed: December 5, 2003 Examiner: TBD  
For: Structure and Method of a Strained Channel Transistor and a Second  
Semiconductor Component in an Integrated Circuit


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Date of Deposit: June 10, 2004

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Respectfully submitted,

  
Kristy Engeldahl  
Legal Assistant

Slater & Matsil, L.L.P.  
17950 Preston Rd., Suite 1000  
Dallas, TX 75252  
Tel: 972-732-1001  
Fax: 972-732-9218



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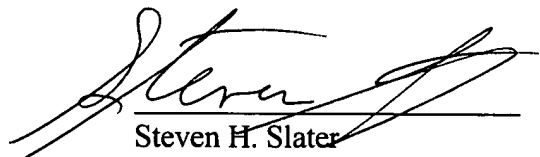
**INFORMATION DISCLOSURE STATEMENT**

The Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed form PTO/SB/08A that may be considered material to the examination of the above-identified application.

This Information Disclosure Statement is submitted under 37 C.F.R. §1.97(b) as no Office Action on the merits has been received. If any fee is required, please charge any additional amount, or credit any overpayment to Deposit Acct. No 50-1065 of the below mentioned firm.

Respectfully submitted,

June 10, 2004  
Date

  
Steven H. Slater  
Attorney for Applicant  
Reg. No. 35,361

Slater & Matsil, L.L.P.  
17950 Preston Rd., Suite 1000  
Dallas, TX 75252  
(972) 732-1001 (phone)  
(972) 732-9218 (fax)

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